

**LOW NOISE DATA CONVERSION CIRCUITS AND
METHODS AND SYSTEMS USING THE SAME**

ABSTRACT:

[0044] A circuit including a first element sampling noise from and discharging noise to a signal line in response to an input signal transitioning on selected edges of a clock signal. A second element samples noise from and discharges noise to the signal line in response to another input signal transitioning on other edges of the clock signal differing from the selected edges of the clock signal such that noise coupled into substrate and supply are independent of the input signal.

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